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DUANE MORRIS LLP
101 WEST BROADWAY, SUITE 900
SAN DIEGO, CA 92101
PHONE: 619.744.2200
FAX: 619.744.2201

FACSIMILE TRANSMITTAL SHEET

In re application of: Yeu-Der Chih

Examiner: Huan Hoang

Serial No.: 10/785,522

Group Art Unit: 2827

Filed: 02/24/2004

Confirmation No.: 8308

For: AN ARRAY STRUCTURE OF TWO-TRANSISTOR CELLS WITH MERGED
FLOATING GATES FOR BYTE ERASE AND RE-WRITE IF DISTURBED
ALGORITHM

Entry approved
HST
2/1/06

To: Examiner Huan Hoan
Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

FACSIMILE NUMBER: 571-273-8300

CONFIRMATION

TELEPHONE:

FROM:

Mark J. Marcelli

DIRECT DIAL: 619.744.2243

DATE: January 25, 2006

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14

MESSAGE: Attached is the Response To Office Action.

NOTE: Original will not follow

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